



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

**\*BIBDATASHEET\***

CONFIRMATION NO. 7097

Bib Data Sheet

SERIAL NUMBER 09/936,487	FILING DATE 01/25/2002  RULE	CLASS 714	GROUP ART UNIT 2133	ATTORNEY DOCKET NO. 15675P372
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

APPLICANTS

Dominique Barthel, Bernin, FRANCE;

\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a 371 of PCT/FR00/00559 03/07/2000 *DG*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *DG*  
 FRANCE 99/02823 03/08/1999

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	STATE OR COUNTRY FRANCE	SHEETS DRAWING 3	TOTAL CLAIMS 27	INDEPENDENT CLAIMS 3
---	---	-------------------------------	------------------------	-----------------------	----------------------------

Verified and Acknowledged  
 Examiner's Signature: *DG Barthel* Initials: *DG*

ADDRESS  
 Blakely Sokoloff Taylor & Zafman  
 7th Floor  
 12400 Wilshire Boulevard  
 Los Angeles, CA  
 90025

TITLE  
 Method for testing integrated circuits with memory element access

FILING FEE  RECEIVED 1548	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
------------------------------------	---	--